

FACULTY PROFILE



Name : **Aswand B**
Post : **Assistant Professor**
Department : **Electronics and Communication Engineering**
Email-id : **basaswa13@gmail.com**

Educational Qualifications:

Examination Passed	Specialization	Year of Graduation	Mode of Study	Name of the College
PG (M.E.)	VLSI Design	2020	FT	Government college of Technology, Coimbatore
UG (B.E.)	Electronics and Communication Engineering	2018	FT	Government college of Engineering, Salem

Industry Experience:

Designation	Period	Organization
Chip design Verification Engineer	13-12-2024 to 03-12-2025	Scaledge India PVT. Ltd., Bangalore
Design Verification Engineer	12-11-2023 to 08-12-2024	WaferMatrix, Bangalore
Associate Chip design verification Engineer & Intern	03-01-2022 to 31-10-2023	Edveon Technologies, Chennai

Total Years of Experience = 3 Years & 6 Months

FDP(s)/Workshops/Conferences:

Title	No of Weeks	From	To
Naan Mudhalvan, Embedded C and Microcontroller Programming	6 days	29-12-2025	04-01-2026
12 th National conference on signal processing, Communication &VLSI Design	2 day	12-06-2020	13-06-2020 -

Publications:

Delay variation model for wallace tree multiplier and 4 Ratio 2 compressor	International Journal of Scientific Development and Research	Oct- 2019,Vol.IV, Issue 10
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